Die Technology Ltd

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Volume Semiconductor Supplier

2 1 14 13 3	PAD FUNCTION 1
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The information given is believed to be correct at the time of issue.

Please verify your requirements prior to commencement of any assembly process, as no liability for omission or error can be accepted. Chip back potential is the level at which bulk silicon is maintained either by bond pad connection or in some cases the potential to which the chip back must be connected if stated above.

Note: 1 mil = 0.001 inch

APPROVED		DIE INFORMATION
D Markham	54F04	DIMENSIONS (Mils): 45 x 41 x 15 BOND PADS (Mils): 4 x 4 min
DATE: 06/02/03		MASK. REF: GEOMETRY: BACK POTENTIAL: GND
SERIAL NUMBER. 000604	WIOTOROLA	METALLISATION TOP: AI BACK: Si